# **Timed Systems and Uppaal**

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## More features of Uppaal

#### Verification of Timed Systems via Uppaal

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More features of Uppaal

## Uppaal

#### Editor

- Templates and instantiations
- Global and local declarations
- System definition

Simulator

• Viewers: automata animator

Verifier (we will explore this later on)

Answers questions about the system at hand

- data expressions over bounded integer variables (e.g int[2..45] x) allowed in guards, assignments, and invariants
- rich set of operators over integer and Booleans, including bitwise operations, arrays, and initialisers
- non-standard types of synchronisation
- non-standard types of location

- Declared as broadcast chan channelname
- a sender can synchronise with an arbitrary number of receivers
- any receiver than can synchronise in the current state must do so
- broadcast sending is never blocking (the send action can occur even with no receivers).



Declared as urgent chan channelname

Both edges need to be taken as soon as they are ready (simultaneously in locations  $\ell_1$  and  $s_1$ ). In other words, no delay allowed if a synchronisation transition on an urgent channel is enabled. Note the problem cannot be solved with invariants because locations  $\ell_1$  and  $s_1$  can be reached at different moments

 Edges using urgent channels for synchronisation cannot have time constraints (i.e. clock guards)



- Time cannot progress in an urgent location
- Both models are equivalent,
- but the use of urgent locations reduces the number of clocks in a model and simplifies analysis

## **Extension: Committed Location**



- Delay is also not allowed and one of the (possibly several) committed locations must be exited in the next instant, i.e. the next transition of the whole system must involve an outgoing edge of at least one of the committed locations
- Our aim is to pass the value k to variable j (via global variable t)
- location n is committed to ensure that no other automaton can change t before the assignment j := t

More features of Uppaal

## Verification of Timed Systems via Uppaal

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### The Satisfaction Problem

Given a timed automaton ta and a property  $\phi$  show that

$$\mathcal{T}(\textit{ta}) \models \phi$$

- in which logical language shall  $\phi$  be specified?
- how is ⊨ defined?

# A variant of Computation Tree Logic (CTL) with two types of formulae $% \left( \mathcal{C}_{1}^{2}\right) =\left( \mathcal{C}_{1}^{2}\right) \left( \mathcal{C}_{1$

- state: for describing properties of states in  $\mathcal{T}(ta)$
- path: for describing properties of paths in  $\mathcal{T}(ta)$

Clock constraints which are used for guards and invariants and similar constraints over integer variables:

x >= 8, i == 8, and x < 2, ...

Additionally,

- *ta.ℓ*, which tests current location: (ℓ, η) ⊨ *ta.ℓ* provided (ℓ, η) is a state in T(*ta*)
- deadlock:

 $(\ell,\eta) \models \forall_{d \in \mathbb{R}_{\geq 0}}$  there is no transition from  $\langle \ell, \eta + d \rangle$ 

 $\Pi ::= A \Box \Psi \mid A \Diamond \Psi \mid E \Box \Psi \mid E \Diamond \Psi \mid \Phi \rightsquigarrow \Psi$ 

 $\Psi \, ::= ta.\ell \mid g_c \mid g_d \mid \texttt{not} \ \Psi \mid \Psi \ \texttt{or} \ \Psi \mid \Psi \ \texttt{and} \ \Psi \mid \Psi \ \texttt{imply} \ \Psi$ 

where

- A, E quantify (universally and existentially, resp.) over paths
- □, ◊ quantify (universally and existentially, resp.) over states in a path

also notice that

$$\Phi \rightsquigarrow \Psi \stackrel{\text{abv}}{=} A \Box (\Phi \Rightarrow A \diamondsuit \Psi)$$

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## Path Formulae pt. II



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## Path Formulae pt. III



### Example

If a message is sent, it will eventually be received send(m) → received(m)

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## $E \diamondsuit \phi$

Is there a path starting at the initial state such that a state formula  $\phi$  is eventually satisfied?

## Examples:

- Is it possible for a sender to send a message?
- Can a message possibly be received?
- Is it possible to reach a certain location of the automaton?

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## $A\Box\phi$ and $E\Box\phi$

## Something bad will never happen

Example:

- In a nuclear power plant will the temperature of the core be always under a certain threshold?
- Will we ever reach a state with a deadlock?

#### $\textit{A} \Diamond \phi \text{ and } \phi \rightsquigarrow \psi$

Something good will *eventually happen* or if *something* happens then *something else* will eventually happen

Examples:

- Always when pressing the on button the television will eventually turn on
- In a communication protocol any message that has been sent should eventually be received

Write the sentences below in CTL

- 1. The system never enters in deadlock
- 2. The location  $\ell$  is reachable
- 3. In all executions we reach location  $\ell$
- 4. If we reach location  $\ell$  we will inevitably reach location s
- 5. There exists at least one execution where variable i is always below or equal 10
- 6. The two philosophers never eat at the same time